

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

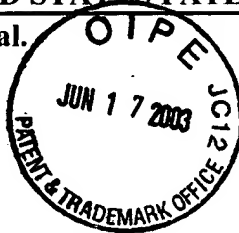
Applicant(s): Hayashida et al.

Serial No.: 09/939,870

Filed: August 27, 2001

Title: Semiconductor Integrated Circuit  
Device and Method for Designing the  
Same

Attorney Docket No.: N26180400W



Group Art Unit: 2836

Examiner: Nguyen, D.

# 8/B  
F.BELL.  
6-25-03

RESPONSE TO OFFICE ACTION

5 Mail Stop Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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10 A. Introductory Comments

The following is submitted in response to the Office Action dated March 17, 2003, and is  
currently due June 17, 2003.

15 This response is presented in the revised amendment format set forth in 1265 Off. Gaz.  
Pat. Office 87 (Dec. 17, 2002) and so is subject to waiver of the provisions of 37 C.F.R. §1.21(a),  
(b) and (c).

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25  
37 C.F.R. §1.8

I hereby certify that this correspondence is being

30 [ ] transmitted via facsimile to the United States Patent and Trademark Office to fax number: \_\_\_\_\_  
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[X] deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents,  
P.O. Box 1450, Alexandria, VA 22313-1450.

35 Date of Deposit: June 13, 2003

Typed/Printed Name: Darryl G. Walker

40 Signature: Darryl G. Walker